THE EASYIC INTERNSHIP PROGRAM

Internship Project

- Head to Tail IC Design or Verification Project
- Create Design & Verification Specifications
- Develop and Verify Design IP Blocks
- Use SystemVerilog & Universal Verification Methodology (UVM)
- Utilise Industry Standard Development Tools
- Work with Standard CPUs and Interfaces
- Project Presentation in English to EasyIC Engineers

Training Modules

- Overview of the IC Development Flow
- Introduction to System-on-Chip Architecture
- Introduction to IC Design with RTL
- Introduction to IC Verification with SystemVerilog/UVM
- "Getting into the Mindset" of a Verification Engineer
- Effective Communication for Engineers

Program Testimonials from recent Interns

"A challenging and stimulating internship!"

"Excellent launching pad into the world of chip development"

"EasyIC engineers were very encouraging and helpful"

GIVE YOUR CAREER A GREAT FOUNDATION!
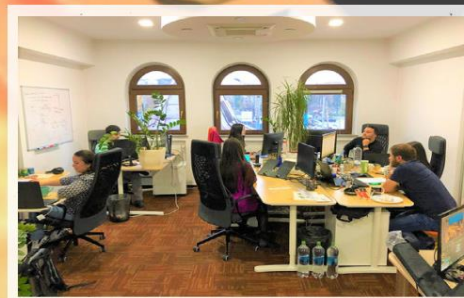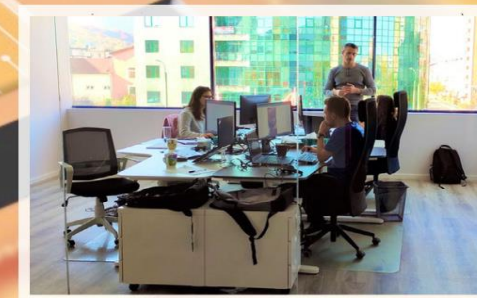
Easy IC

INTERNSHIP PROGRAM HIGHLIGHTS

- Hands-on Training in IC Design & Verification supervised by Experienced Engineers
- Execute IC Project from Specification to Sign-off
- Work with Industry Standard Languages, Tools & Methodologies
- Technical & Soft Skills Training Modules
- Mentored by an EasyIC Engineer
- Technical English Working Environment
- Work with an Arm Approved Design Partner & Xilinx Certified Partner
- Following Internship, opportunity to be hired full or part-time

Bucharest office

Brasov office

---

arm
APPROVED DESIGN PARTNER

XILINX.
Certified Partner